

TECHNICAL COMPETENCE!

1. Complete mask making facilities!
2. Advanced testing techniques!
3. Automated design facilities!
4. Advanced circuit design techniques!
5. Ultra modern manufacturing facilities!

ADVANCED TECHNOLOGY!

MOS TECHNOLOGY INC. is dedicated to a continuous program of advanced development to retain technical leadership in MOS arrays. Special interest areas include:

1. Early production of a stable low voltage providing direct interface with bi polar integrated circuits.
2. Implementation of automatic design and testing capabilities.
3. Development of advanced packaging techniques.
4. Continuous development of circuit and processing techniques to provide greater array density and higher operating speeds.

MANUFACTURING!

1. A 60,000 square foot ultra modern plant with planned expansion to 160,000 square feet.
2. Development of automated process and assembly techniques to achieve higher production levels and to reduce labor and test costs.



MOS TECHNOLOGY INC., VALLEY FORGE INDUSTRIAL PARK, VALLEY FORGE, PENNSYLVANIA



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Introducing

MOS TECHNOLOGY INC.!! EXCLUSIVELY DEDICATED TO MOS APPLICATIONS!!

MOS has widespread usage for . . .

- Computer terminals
- Computer printers
- Computer displays
- Computer memories
- Entertainment systems
- Medical electronics
- Industrial controls
- Electronic instruments
- Electronic calculators
- Home appliance controls
- Communications systems
- Instrumentation systems

MOS TECHNOLOGY INC.
VALLEY FORGE INDUSTRIAL PARK
VALLEY FORGE, PENNSYLVANIA

Management Team



PRESIDENT, JOHN PAIVINEN was most recently Vice President and General Manager of the microelectronics division of a major electronics company. Mr. Paivinen also brings experience of 16 years in the computer industry and 6 years of general management.



VICE PRESIDENT AND DIRECTOR OF MARKETING, LEONARD SMITH was personally responsible for all phases of MOS marketing for a major supplier over the past 4 years. His experience in component sales, electronic systems sales and quality assurance provide a systems and quality orientation of special interest to customers. Mr. Smith has been in supervision and management positions for the past 14 years.



VICE PRESIDENT AND DIRECTOR OF ENGINEERING AND MANUFACTURING, DONALD McLAUGHLIN was most recently director of engineering reporting to Mr. Paivinen. He was responsible for all engineering tasks including array design, prototype production, process development, photomask production, and implementation of techniques for automatic design and array testing. Mr. McLaughlin's experience includes 12 years in data processing systems and 7 years in management.

Experienced MOS Management has been assembled to plan and execute MOS TECHNOLOGY INC. operations. A balance of business, technical and marketing skills is available within this management. They have operated successfully in the MOS field for more than two years.



VICE PRESIDENT AND DIRECTOR OF OPERATIONS ADMINISTRATION, MORTON JAFFE brings management experience through the general manager level to the support functions of the company. His experience covers project management and contract negotiations on programs up to \$200,000,000 in value, with overseas management experience in his last two positions.

MANAGER OF PROCESS ENGINEERING AND TECHNOLOGY, MR. WALTER EISENHOWER brings over 14 years of pertinent experience in the fields of hybrid and monolithic integrated circuits design and fabrication. In his most recent position he was responsible for advanced process development for MOS arrays. He is experienced in all phases of planar processing, assembly techniques, passivation methods, device design, and reliability evaluation. His prior experience includes semiconductor process development at ITT, Burroughs, and Western Electric.

MANAGER OF PHOTOMASK DEPARTMENT, MR. DONALD PAYNE brings to MOS TECHNOLOGY INC. 20 years of experience in the field. Eleven years as a project engineer in the advanced development department of Philco, 5 years as General Manager of Phototronics Division of Biskay, Photomask Manufacturers, and 2 years as Manager of the Photomask Department of General Instrument Microelectronics Division.



To Our Customers

In the last six years, MOS has become recognized as a viable and competitive technology for manufacturing integrated circuits. Circuits can now be offered commercially with more than 2000 transistors on a single silicon chip and complexities of 400 to 1000 transistors are relatively routine.

Customer acceptance of MOS is now widespread. Cost, Reliability, Size, and Power are major reasons for this acceptance. Customer experience indicates a savings advantage of 30% to 50% in the cost of producing electronic products, including all elements of component costs, assembly and test labor.

The economies available from MOS devices are universally available to all industries. As long as an application can be satisfied with reasonably high speed circuit performance and supported with reasonable production volume, it can, and should, be implemented by MOS.

MOS TECHNOLOGY INC. is a company founded *exclusively* to serve the customer with the highest quality and technically competent custom and standard MOS arrays attainable. Each member of the management has had broad experience in the development of the various MOS processes, circuit design, testing techniques, manufacturing, and quality control. All of these vital in serving the customers needs. We realize at MOS TECHNOLOGY, INC., that only by fulfilling the customer's needs with a quality product will we insure our success.

JOHN PAIVINEN, *President*