APRIL, 1976

We know you will be pleased that MOS TECHNOLOGY, INC. and Motorola have settled their suit and countersuit and have signed a patent cross license covering both company's microcomputer lines. We really appreciated your expressions of support during the past few months. As part of the overall settlement, MOS TECHNOLOGY, INC. has agreed to withdraw the MCS6501 from the marketplace - thus ending any sensitivity about compatibility.

Now that we're at peace, here's some good news for everyone on our mailing list - we are introducing five new microprocessors in June and three new I/O products later in the summer. The MCS6506 is a 28 lead microprocessor with both ϕ_1 (OUT) and ϕ_2 (OUT) made available and with on-the-chip clock. The MCS6512, MCS6513, MCS6514, and MCS6515 are the counterparts, functionally, to the MCS6502, MCS6503, MCS6504 and MCS6505 with the difference being a two phase clock input on the new products. This line of new microprocessors is especially suited to multi-processor systems where maximum control of timing relationships is of paramount importance as well as utilization of memory sharing wherever possible to save on system costs. Included in this newsletter are pinout diagrams of the entire current microprocessor family (all nine microprocessors) which are all software compatible and will be available in maximum frequencies of 1MHz and 2MHz. The new devices will be available for sampling in June at the same low prices you have come to expect from MOS TECHNOLOGY, INC. The 1-99 pricing will be $20.00 for the 40 lead MCS6512 and 28 lead MCS6506 with on-board clock, and $18.00 for the 28 lead MCS6513, MCS6514 and MCS6515.

New I/O products due for introduction in the next few months will include the MCS6520, MCS6522 and MCS6532.

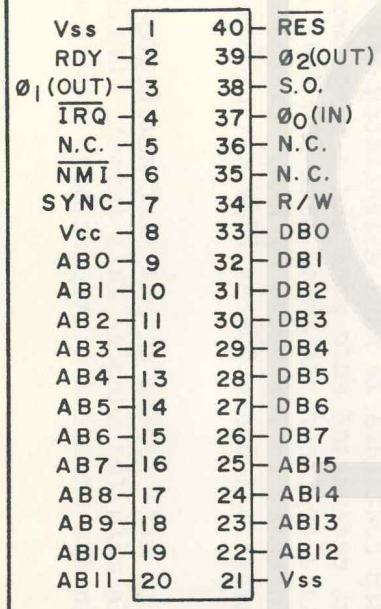
The MCS6520 is a direct replacement for the MC6820, Motorola's "Peripheral Interface Adapter". As such, it will contain the same powerful features (data direction registers, control register, dual eight bit peripheral ports, handshake capability, etc.) as the popular "PIA". We would like to point out that this chip was designed by our second source, Synertek, in Santa Clara - the relationship between the two companies is indeed one characterized by efficiency, mutual support, and above all productivity.

The MCS6522 will contain essentially the same basic features of the MCS6520 and in addition will include latching on the peripheral data ports, a register for serial capability, and two programmable interval timers. Termed the "Versatile Interface Adapter" or "VIA", this product will find use in nearly all microcomputer systems requiring special timing functions and/or serial stream data flow.

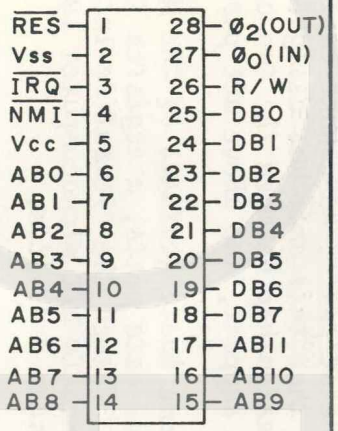
The MCS6532 is similar to our MCS6530 "Combo" chip except we have deleted the ROM but doubled the RAM size to 128 x 8. The chip continues to have essentially the same I/O and Timer features with 16 bi-directional peripheral data pins and a programmable interval timer. The chip is designed for those applications where more RAM than the 64 bytes of the MCS6530 is needed - hence the increase to 128 bytes of RAM. Exclusion of the 1024 x 8 ROM allows the user to go to larger off board ROM or PROM for program storage.

More details on all of our new products will follow in our next newsletter, with data sheets on the new I/O products available with the samples this summer. Until then, thanks for your support and interest in the broadest microprocessor family in the industry.

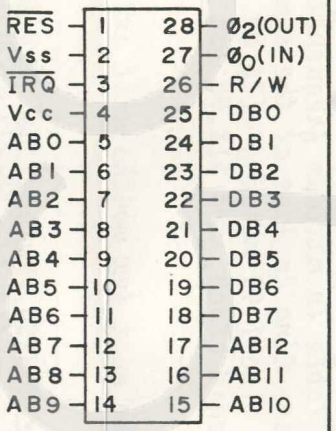
MOS TECHNOLOGY, INC.



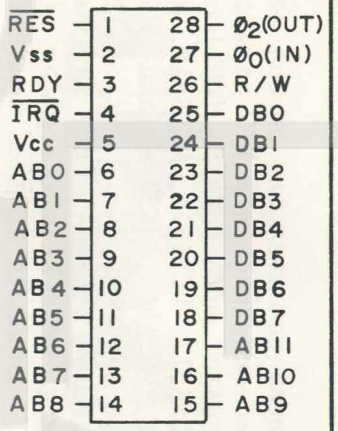
MCS6502



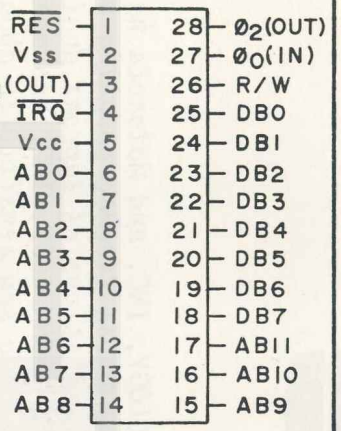
MCS6503



MCS6504



MCS6505



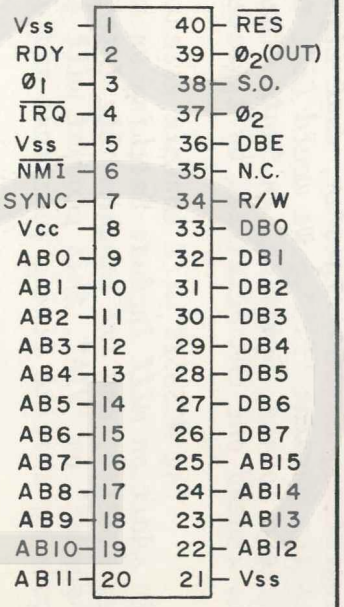
MCS6506

MCS6500
MICROPROCESSOR
FAMILY

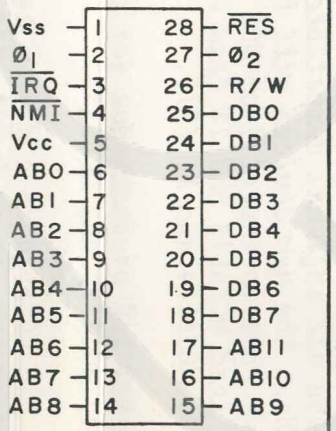
*9 MICRO'S

*ALL SOFTWARE
COMPATIBLE

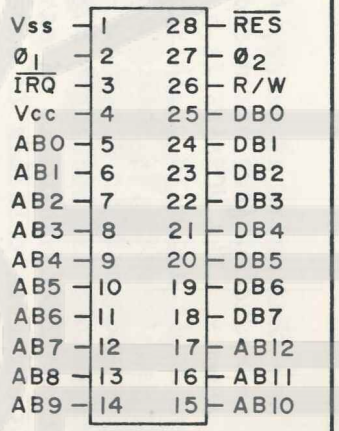
*UP TO 2MHz
CLOCK RATE



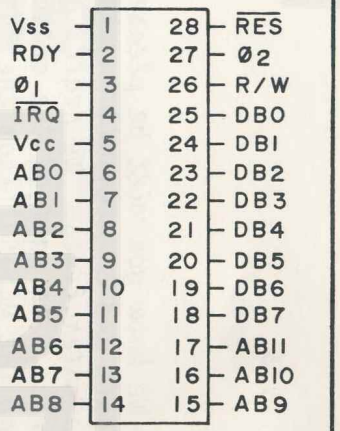
MCS6512



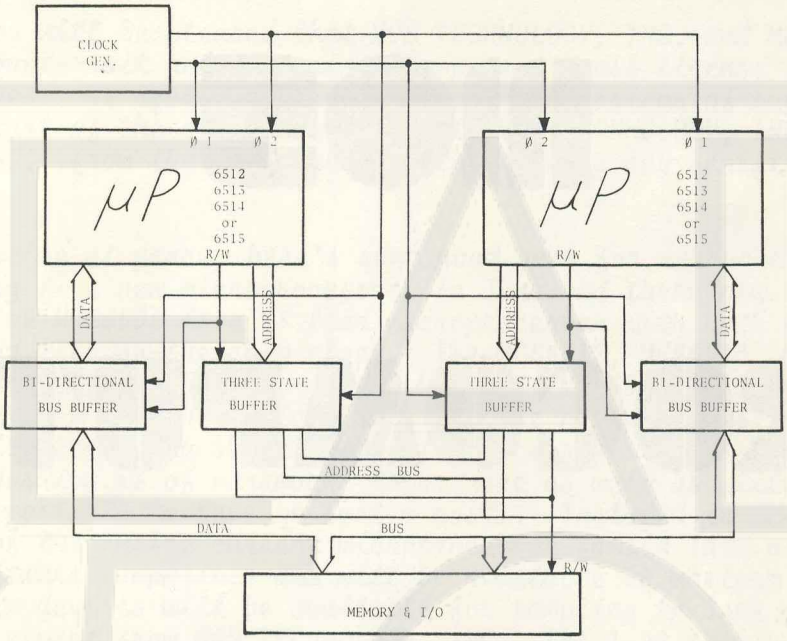
MCS6513



MCS6514

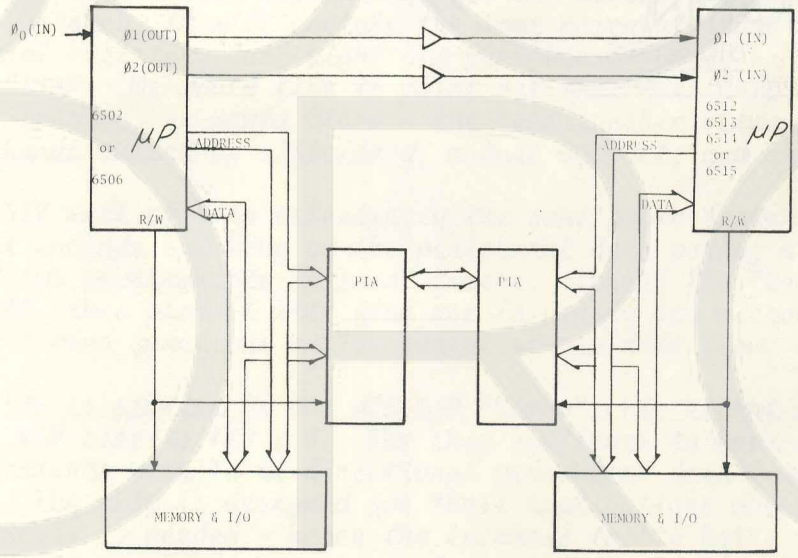


MCS6515



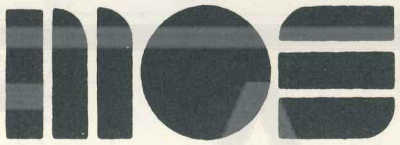
This configuration allows two microprocessors to share memory and I/O by accessing memory during opposite phases of the system clock.

MULTIPLE PROCESSORS DRIVEN FROM EXTERNAL CLOCK



This configuration allows multiple processors to communicate through peripheral adaptors.

MULTIPLE PROCESSOR SYSTEMS DRIVEN BY ON-CHIP CLOCK



MICROCOMPUTERS

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